## Universal Probe Blue

Supports electronic products development with various functions.





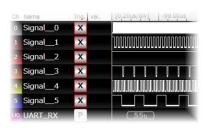


You can use powerful WATCHPOINT Debugger. Three types are available Cortex-M, ARM, MeP.

# WATCHPOINT Writer option

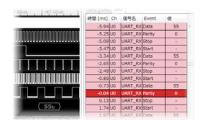
This writer can erase and write ARM microcontroller internal/outside flash memory and is useful at production site from small to mass production.

#### Logic Analyzer



This 8 channel Logic Analyzer catches flowing signals surely at up to 100MHz sampling and is useful to solve the problems of low repeatability because of the long-time recording on HDD.

#### **Protocol Analyzer**



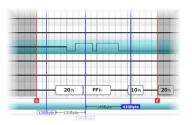
It performs protocol analysis for waveform of I2C, URT, SPI and CAN acquired by Logic Analyzer and also can trigger to protocol.

#### **Boundary-Scan Tool**



Boundary-Scan Tool can check the condition of SMT components to which boundary-scan of JTAG cannot perform probing and the connection on printed circuit board.

#### **Pattern Generator**



This 8 channel Pattern Generator can output digital signals of 20 Mbps at maximum. Output simulation of the correct signal leads you to isolate the cause of the problem.

**Product Site** 

http://www.ss-technologies.co.jp/en/products/u\_prb\_b/index.html



### **Product Specifications**

#### WATCHPOINT Debugger (option)

Our powerful debugger. Three types are available ARM, Cortex-M and Toshiba MeP.

#### WATCHPOINT Writer (option)

Support JTAG/SWD and this for deal with a variety of devices. The maximum writing speed is 300KByte/s.

#### Logic Analyzer

The Logic Analyzer can be used in most digital devices because the signal level supports from 0.0V to +5.0V. When more channels are needed, the number of channel can be increased up to 32 channels by connecting 4 units Universal Probe Blue.

(However, the maximum sampling rate will be 10MHz.)

Recording capacity depends on the sampling rate.



The objective phenomena can be detected more simply by the various trigger modes.

The protocol trigger can trigger at the point where the error occurred.



#### Protocol Analyzer

The Protocol Analyzer analyzes the protocol of I2C, SPI, UART and CAN signals recorded by Logic Analyzer. The result of analysis can be displayed with the waveform of signals and the analysis list can be saved to a file.

#### CMSIS-DAP

It supports both JTAG and SWD interfaces, and can be used in the following IDE:

- IAR Embedded Workbench for ARM
- KEIL MDK-ARM
- Eclipse

#### Boundary-Scan Tool

The Boundary-Scan Tool supports many major JTAG devices and can check the terminal condition of components such as BGA and QFN to which probing cannot be performed.

It also can support new devices by adding BSDL files.

#### Pattern Generator

This 8 channel Pattern Generator can output digital signals up to 2.5 Mbps and also control the high impedance output for each channel.

The length of pattern to be output is 12kbit/channel at maximum. The supported output voltage is 0.0V to +3.3V.





#### Sohwa & Sophia Technologies

We have continued to pursue technology to meet engineers' needs and create new products as a development tool maker for 38 years. Our tools have been supporting product development of domestic and foreign semiconductor companies. "Universal Probe Blue" is a product combining the best technologies in our experiences. This tool will enable you to reduce your development time and market your products quickly.

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- The numerical values shown here are according to our own calculation.

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