

Universal Probe

One unit with 6 functions to support the development of electronic equipment



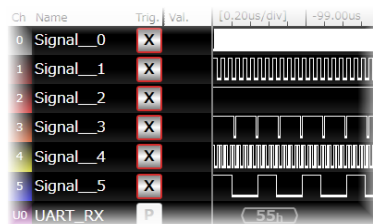
(Actual size)
W:119 × D:69 × H:14.1mm

CMSIS-DAP



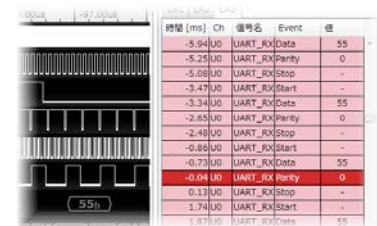
You can quickly start to develop embedded software by connecting not only to "Eclipse" available for free but also to the integrated development environment of IAR Systems or ARM.

Logic Analyzer



This 8 channel Logic Analyzer catches flowing signals surely at up to 100MHz sampling and is useful to solve the problems of low repeatability because of the long-time recording on HDD.

Protocol Analyzer



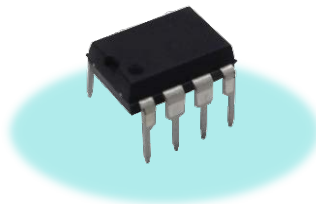
It performs protocol analysis for waveform of I2C, URT, SPI and CAN acquired by Logic Analyzer and also can trigger to protocol.

Boundary-Scan Tool



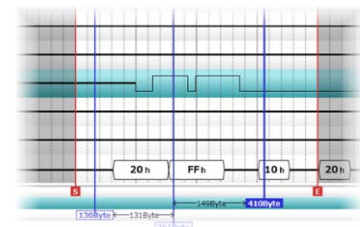
Boundary-Scan Tool can check the condition of SMT components to which boundary-scan of JTAG cannot perform probing and the connection on printed circuit board.

ARM/SPI Writer



This writer can erase and write ARM microcontroller and SPI flash memory and is useful at production site from small to mass production.

Pattern Generator



This 8 channel Pattern Generator can output digital signals of 20 Mbps at maximum. Output simulation of the correct signal leads you to isolate the cause of the problem.

Product Site

http://www.ss-technologies.co.jp/en/products/u_prb/index.html

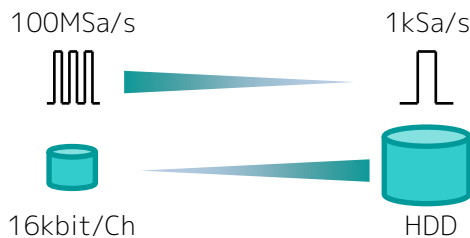
Product Specifications

◆ Logic Analyzer

The Logic Analyzer can be used in most digital devices because the signal level supports from 0.0V to +5.0V. When more channels are needed, the number of channel can be increased up to 32 channels by connecting 4 Universal Probes.

(However, the maximum sampling rate will be 10MHz.)

Recording capacity depends on the sampling rate.



The objective phenomena can be detected more simply by the various trigger modes.

The protocol trigger can trigger at the point where the error occurred.

◆ Protocol Analyzer

The Protocol Analyzer analyzes the protocol of I2C, SPI, UART and CAN signals recorded by Logic Analyzer. The result of analysis can be displayed with the waveform of signals and the analysis list can be saved to a file.

◆ CMSIS-DAP

It supports both JTAG and SWD interfaces, and can be used in the following IDE:

- IAR Embedded Workbench for ARM
- KEIL MDK-ARM
- Eclipse

◆ Boundary-Scan Tool

The Boundary-Scan Tool supports many major JTAG devices and can check the terminal condition of components such as BGA and QFN to which probing cannot be performed.

It also can support new devices by adding BSDL files.

◆ Pattern Generator

This 8 channel Pattern Generator can output digital signals up to 20 Mbps and also control the high impedance output for each channel.

The length of pattern to be output is 12kbit/channel at maximum.

The supported output voltage is 0.0V to +3.3V.

◆ ARM / SPI Writer

The ARM Writer and the SPI Writer support JTAG/SWD and SPI interfaces to deal with a variety of devices.

The maximum writing speed is 300KByte/s.

Contents of this product



Universal Probe



USB cable



Flat cable



Extension cable
(3 pieces)

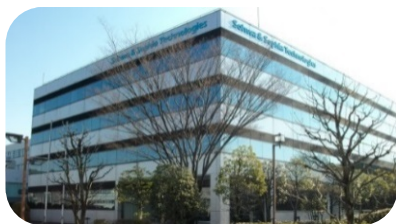


Universal Probe is compliant with the regulations of various countries

Price List

Universal Probe	US\$199
+	
CMSIS-DAP	Free
Boundary-Scan Tool	Free
ARM/SPI Writer (Size-limited)*1	Free
Logic Analyzer/Protocol Analyzer	US\$99
Pattern Generator	US\$49
ARM Writer	US\$99
SPI Writer	US\$99

*1 : The writing data size is limited to 1KByte.



Sohwa & Sophia Technologies

We have continued to pursue technology to meet engineers' needs and create new products as a development tool maker for 38 years. Our tools have been supporting product development of domestic and foreign semiconductor companies. "Universal Probe" is a product combining the best technologies in our experiences. This tool will enable you to reduce your development time and market your products quickly.

Follow me !

@sst_dev

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