

**Product** Brief

# WATCHPOINT Writer for EJSCT ARM



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#### Features

- One common hardware unit supports other CPU series by purchasing software license additionally
- Download capability to flash memory
- Writes to on-board and CPU internal flash memory
- Useable as a stand-alone writer. (operate by AC adaptor)
- One-touch automatic script execution via the PLAY button on the unit
   The external terminal setup for executing a script by signal input and detecting the end state of a script externally
- Perfect for use in auto-verification of the target on mass production or for updating new versions

This software is used in combination with our JTAG emulator "EJSCT".



- Small size (70mm × 108mm × 17mm)
- Powered by USB bus when using in connection with PC
  - Supports ARM7TDMI®, ARM9TDMI®, ARM11, Cortex core (A15, A9MPCore, A8, R4, M0/M3/M4)
     Detailed setup for individual CPU
    - CPU automatic detection
  - Supports half pitch connector of Cortex-M
  - Docking of windows
    - ·Helpful for expansion and arrangement of work space
  - Supports C/C++ (OS: Windows2000/XP/Vista/7 \*1)
  - ■Works on various PC\*2, note PC and other host computers. (by connecting USB)
  - Supports over 300 types of flash memory devices\*4
  - Possible to write to not-supported flash memory when users make a custom program

## **Specifications**

Target CPU	ARM7 core series (ARM7TDMI, ARM7TDMI-S, ARM710T, ARM720T, ARM740T, etc.) ARM9 core series (ARM9TDMI, ARM9TDMI-S, ARM910T, ARM920T, ARM940T ARM9E, ARM946E, ARM966E, ARM922, ARM925, ARM926, etc.) ARM11 core series (ARM1136, ARM1176, etc.) Cortex core series (A9 MPCore, A15, A8, R4, M0/M3/M4)
Target Vcc	VCC 0.8~5.0V
Flash memory Writer capability <sup>*3</sup>	<ul> <li>Memory DUMP capability</li> <li>Data download from files</li> <li>Writes via PLAY button on the unit or Write button on the writer software by recording batch file for writing</li> <li>Writes by inputting signal to external input terminal</li> <li>Block erase capability</li> <li>Stand-alone writer capability</li> <li>Without a command from the computer, users can download to flash memory by recording flash memory writing script into the attached MicroSD.</li> <li>(Two different operation scripts can be recorded)</li> </ul>
Flash memory makers <sup>∗</sup> 4	Renesas Technology, Numonyx (Intel, ST Micro,), MACRONIX, Sharp, Toshiba, NEC, Sanyo, ATMEL, SST, OKI, Spansion (Fujitsu, AMD), EON

\*1 Supports Windows XP Professional x64Edition (64 bit) and Windows Vista/7 (32 bit, 64 bit)

\*2 Please confirm in advance because some machines need an operation check.

\*3 This WATCHPOINT writer supports capability only for download to flash memory. Debugger capability such as breakpoints, registers and step execution are not included. Please purchase WATCHPOINT Debugger license when the EJSCT unit is used as a JTAG debugger.

\*4 Please refer to our website for more details.

### Sohwa & Sophia Technologies Inc.

### Configuration



#### CD-ROM

The above cables support Cortex core, ARM7 core, ARM9 core and ARM11 core CPUs. Other CPUs are NOT supported.



JTAG Cable SCP7500 : SCP 20to20 SCP7200 : SCP CortexMx-PB-HF10-JCB SCP7300 : SCP CortexMx-PB-HF20-JCB VK0019 : TI-ARM exclusive JTAG cable

#### Object Data for Flash memory writing:

Supports the below compilers output

ARM	MDK-ARM, DS-5
IAR	EWARM
GreenHills	GHS
GAIO	XCC-V
GNU	GCC
Metaware	High C/C++/EC++ for ARM

### **Target Connections**

0.1 inch 2×10 Connector		( 0	0.05 inch $2 \times 5$ Connector			0.05 inch 2×10 Connector									
VTRef NC SWDIO SWCLK NC SWO	(nTRST) (TDI) (TMS) (TCK) (RTCK) (TDO)	1 3 7 9 11 13		2 4 8 10 12 14	VSupply GND GND GND GND GND	VTref GND GND KEY GNDDetect Recommend SHF-105-01-	1 3 7 9 led Conni -L-D-** (S	ector:	2 4 6 8 10	SWDIO / TMS SWCLK / TCK SWO / TDO NC/EXTb / TDI nRESET	VTref GND GND KEY GND GND GND GND GND	1 3 7 31 9 11 13 15 17		2 4 8 10 12 14 15 18	SWDIO / TMS SWOLK / TCK SWO/EXTe/TRACECTL / TCI NC/EXTb / TDI nRESET TRACEOLK TRACEOATA[0] TRACEOATA[0] TRACEOATA[1]
DBGRQ DBGACI Recomm VTRef V SCP750	K nended co 'supply Co 0: SCP 20	17 19 onnec onnec onnec 0 to 2	tor: 7620 to to Targe 0	18 20 -6002 (3	GND GND GND Supply	VTRef Connect to Target power supply Option cable: SCP7200 * SCP7200 is required when half pitch 10 pin connector is mounted on the target board.				Recommended connector: SHF-110-01-L-D-** (SAMTEC) VTRef Connect to Target power supply Option cable: SCP7300 * SCP7300 is required when half pitch 20 pil connector is mounted on the target board.					

## WATCHPOINT Software License

The WATCHPOINT Writer for EJSCT is a licensed software product. One common hardware unit supports other CPU series by acquiring and registering additional software licenses.

The WATCHPOINT license operates with the registered EJSCT hardware unit only. Once the license is registered with one EJSCT unit, users need to purchase a new WATCHPOINT license in order to use with another EJSCT unit.

### **Ordering Information**

System r	equirements	Items necessary for standard Writer System						
Host PC *1	Connect with PC	JTAG emulator	Writer Software (WATCHPOINT))	Support Service	JTAG cable (Must select at least 1 cable)			
Windows PC	USB2.0/1.1 Connection	SCD001 : EJ-SCT	SCS0790E : WP WRT for EJS ARM	SSS010 : Sophia Support Service	SCP7500 : SCP 20to20 SCP7200 : SCP CortexMx-PB-HF10-JCB SCP7300 : SCP CortexMx-PB-HF20-JCB VK0019 : TI-ARM exclusive JTAG cable*2			

Items in the box surrounded by bold lines are necessary for Writer Systems. The EJSCT JTAG emulator serves as the hardware key when using software writer license

\*1 Host PC

OS: Windows 2000/XP Professional x64Edition(64 bit) or Windows Vista/7(32 bit, 64 bit) Memory: Minimum 32MB, 64MB+ recommended

Hard Disk: 100MB for installation

- \*2 VK0019 is required to connect with TI 14pin JTAG header.
- \* Product and company names are trademarks or registered trademarks of their respective owners.
- \* Product specifications are subject to change without notice.



### Sohwa & Sophia Technologies Inc.

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(Ver.140408)

## Please contact us about the prices